

TEST ENGINEERING (DEPT 87)

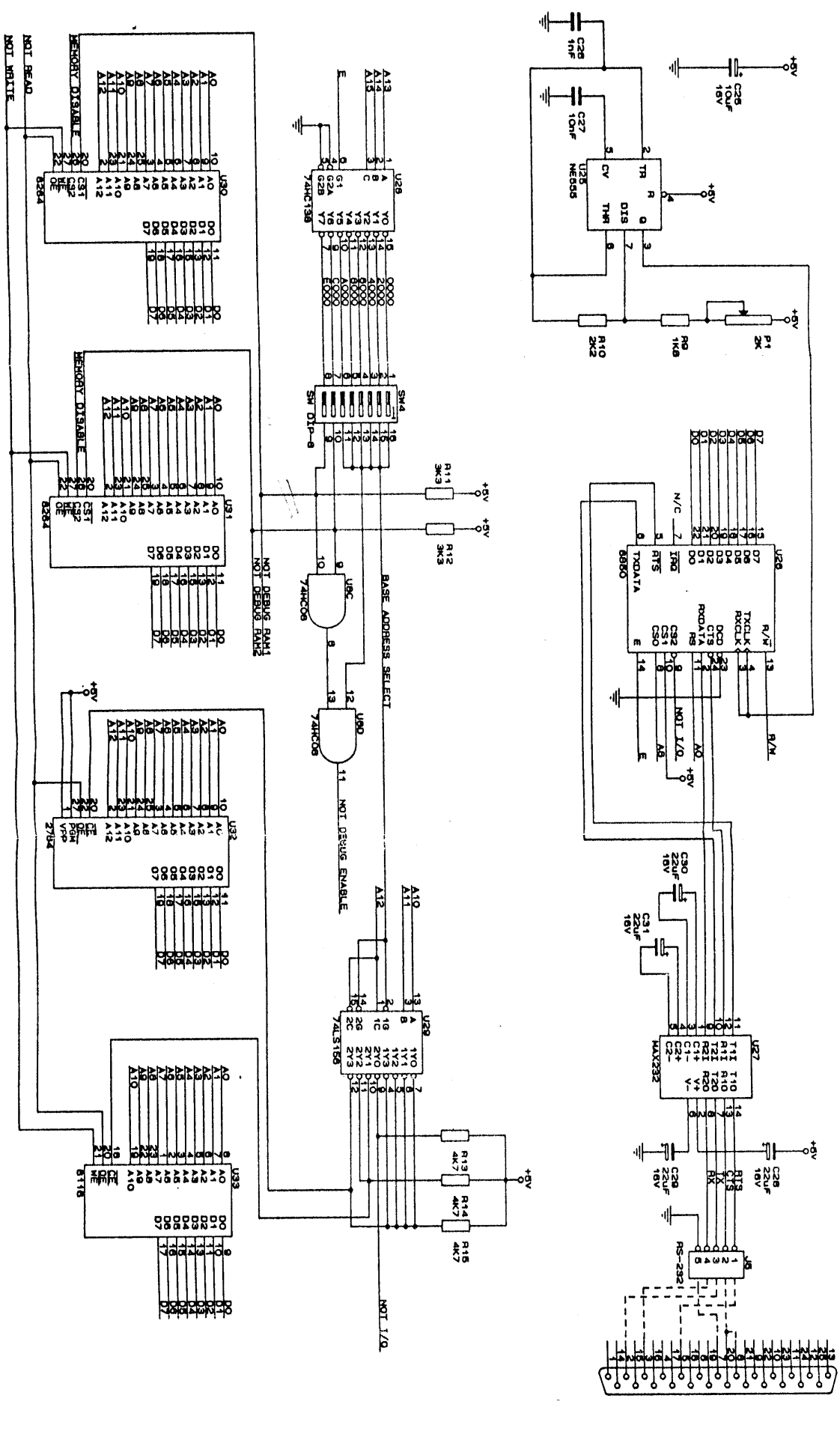
71110

MC88000 EMULATOR

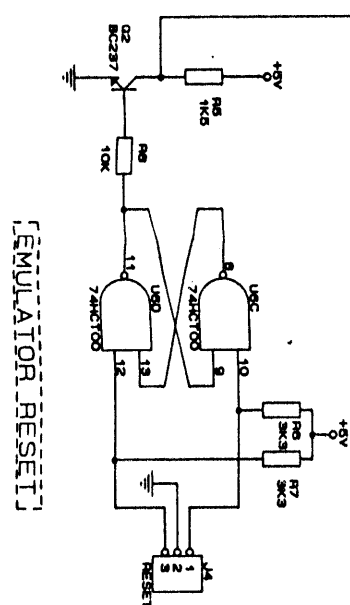
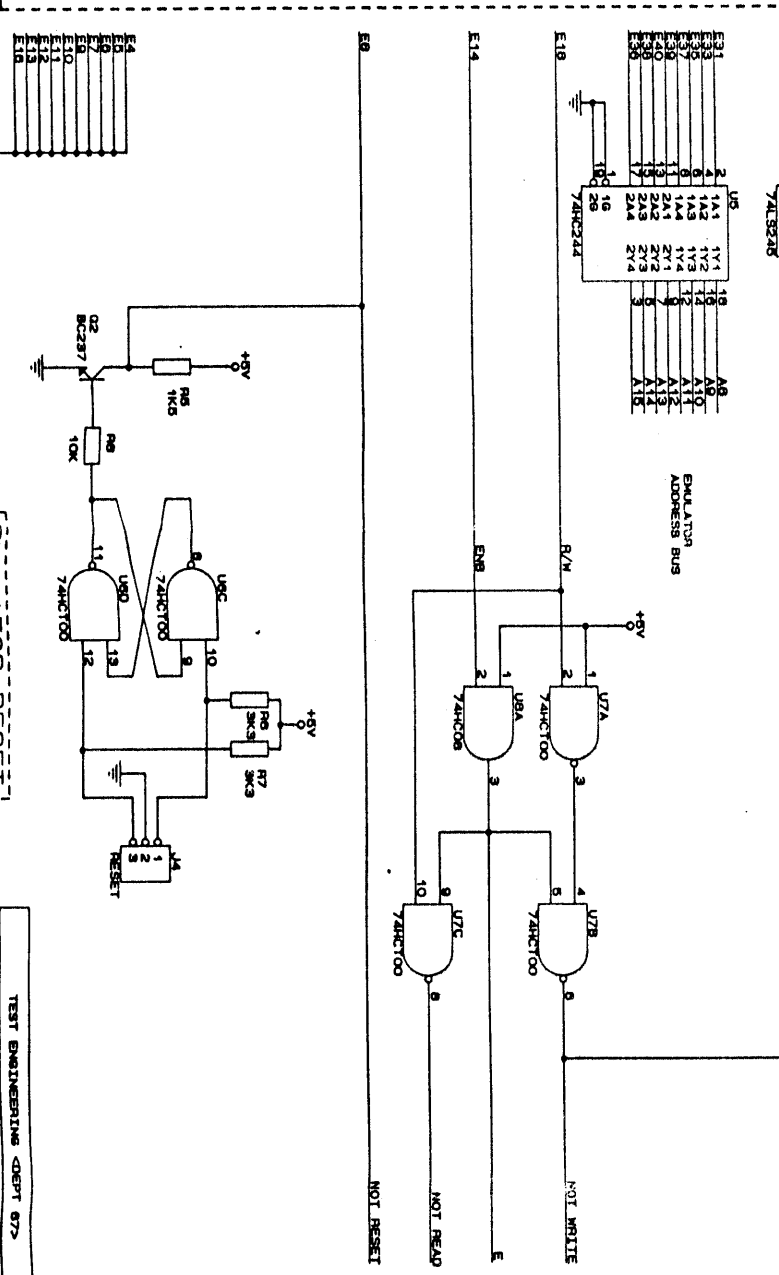
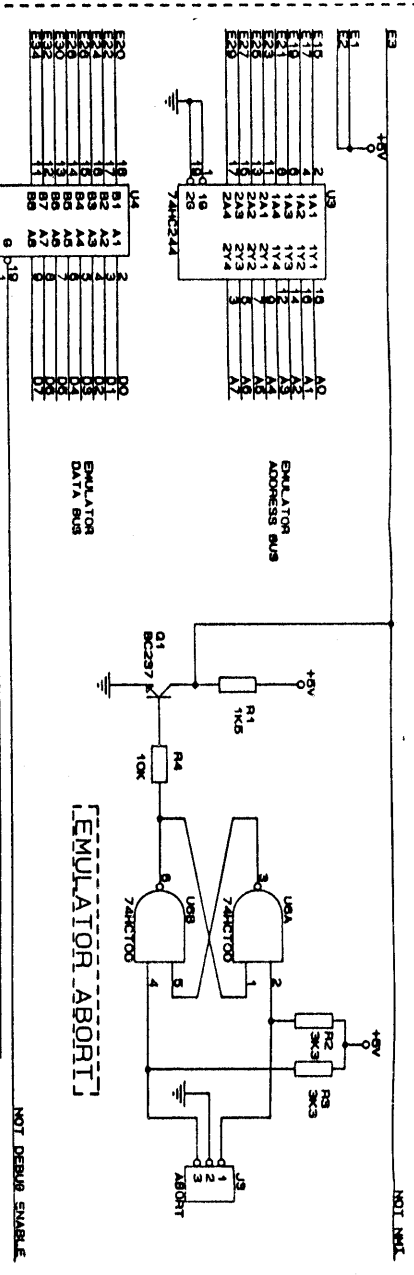
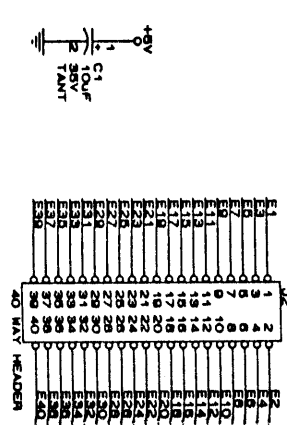
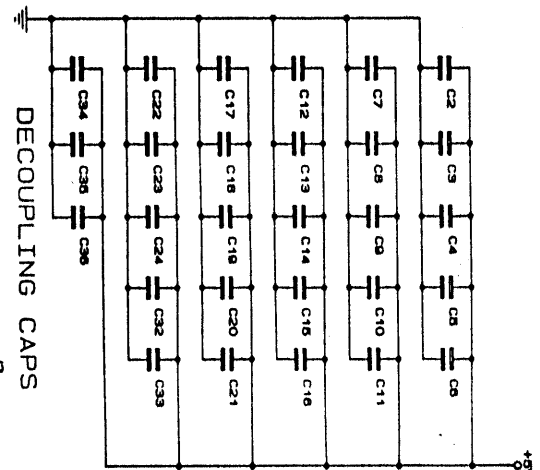
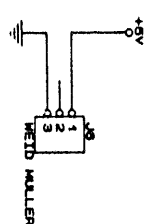
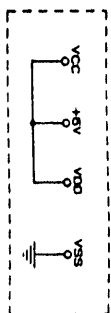
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A2 HARDWARE RESET AND BREAKPOINT OCT. REV

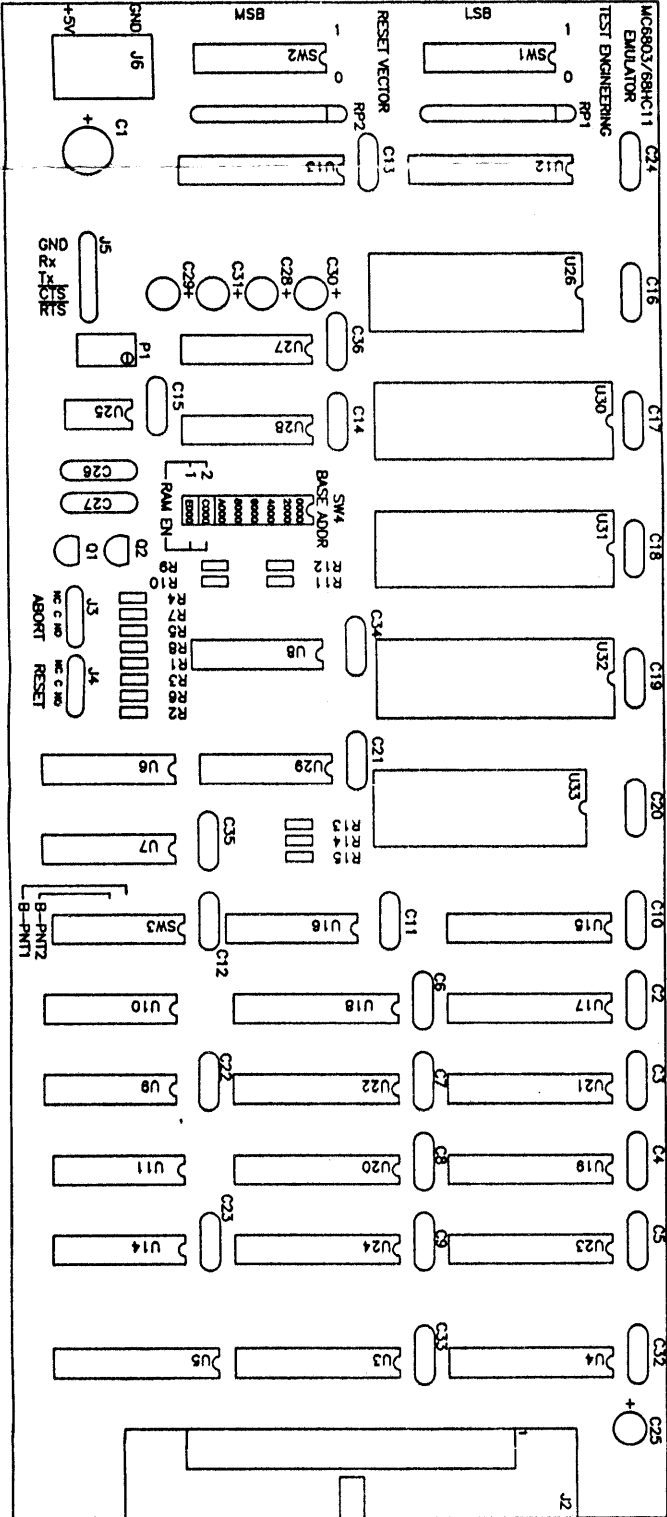
Date: JANUARY 1, 1990 SHAW 1 07 3

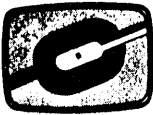


TEST ENGINEERING DEPT 87	
TITLE	
MCS005 EMULATOR	
Size	Document Number
A2	MEMORY AND DECODING OCT.
DATE: 2 JANUARY 1 1980	REV
1	01
3	



TEST ENGINEERING DEPT 877	
MOSBOS EMULATOR	
TITLE	
SIZE	Document Number
A2	POD AND BUFFER CCI.
DATE	2 JANUARY 1 1980
REV	3 07
01	3





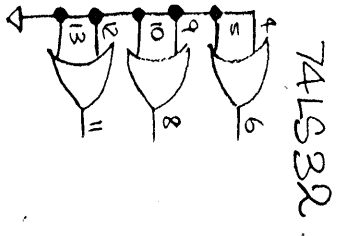
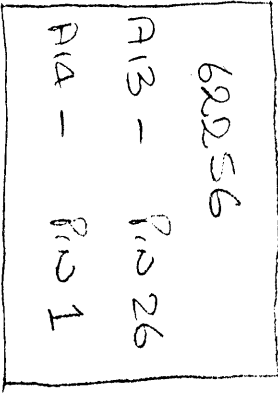
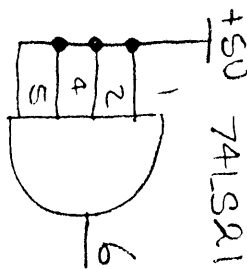
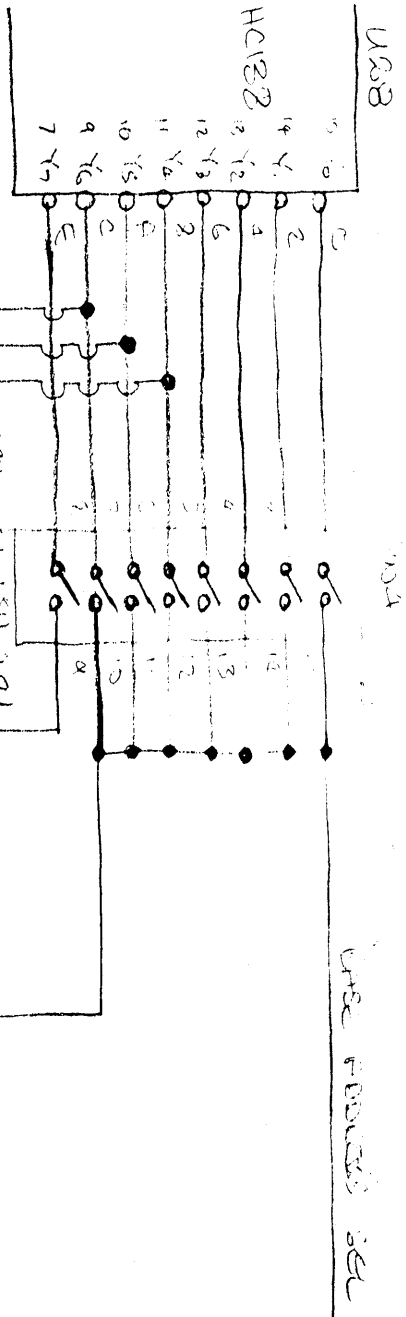
CONLOG

ELECTRONIC CONTROL TECHNOLOGY

MODIFICATION TO EMULATOR TO UPGRADE TO 32K RAM

Date: 3/4/99

Sheet: 1 OF 1



CS OF
32K RAM
62256 Pin 20.

NOT DEBUG
RAM1

MEM
DISABLE

U8D
NOT DEBUG ENABLE

USE PROVIDED SET

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